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Total No of Pg:01

**Examination May –2014**  
**COMPUTER SYSTEM ARCHITECTURE**  
**Paper Code:BS-201**  
**Paper ID-B0409**

Time Allowed: 3 Hrs.

Max Marks –60

**Instructions to Candidates:***1 Section-A is compulsory.**2 Attempt any four questions from Section-B***SECTION –A****(2x10=20)**

- 1) Explain the following.
  - a) Explain the steps involved in an instruction cycle.
  - b) Give examples of data manipulation instruction.
  - c) Explain Isolated I/O and Memory mapped I/O
  - d) Write note on handshaking.
  - e) Write note on memory reference instructions.
  - f) What is the role of program counter?
  - g) What do you mean by paging? a2zpapers.com
  - h) Differentiate between hardwired and micro-programmed control unit.
  - i) Differentiate between internal and external interrupts.
  - j) What is memory stack?

**SECTION-B****(4x10=40)**

- 2 Compare various addressing modes of computer system architecture. Discuss it write some example.
- 3 Discuss various modes of data transfer Explain DMA in detail.
- 4 What are computer registers? Explain how registers can be connected to common bus with the help of load instruction.
- 5 Why is cache memory needed? Explain the working of set associative mapping in cache memory.
- 6
  - a) write note on CPU\_IOP communication.
  - b) What are the various instruction formats? Explain any one of them.
7. Explain about the evolution of computer.